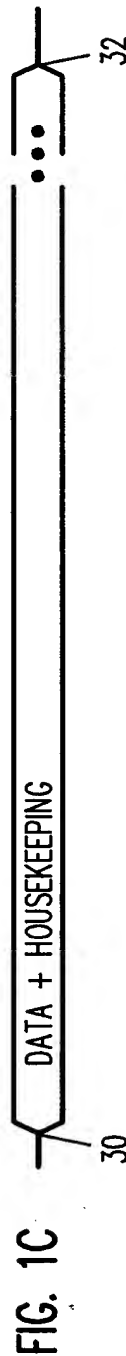
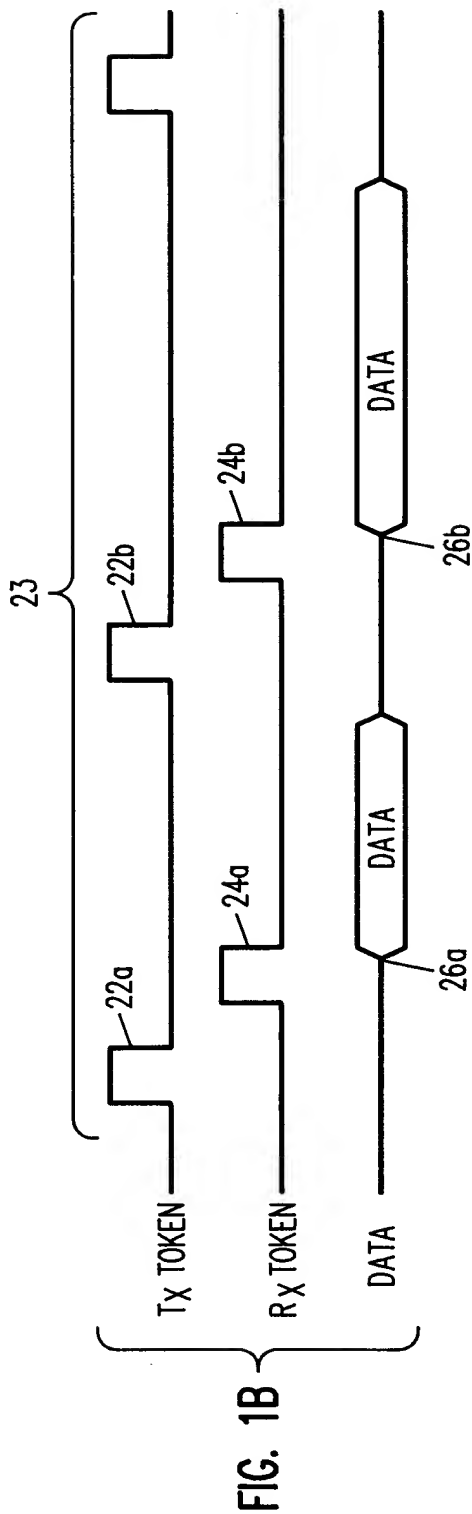
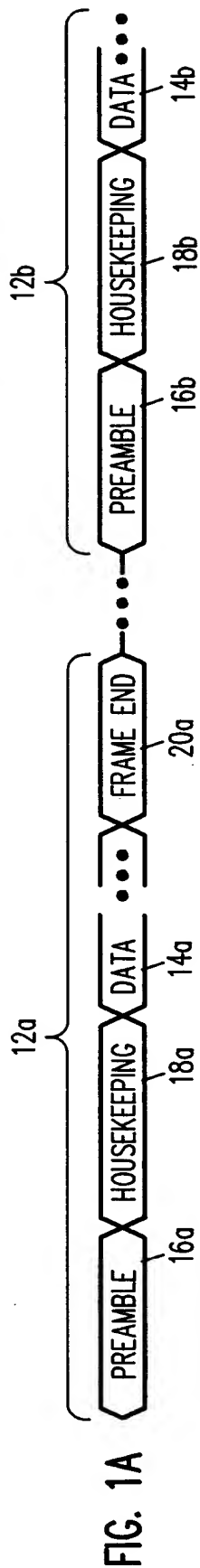


APPROVED	O.G. FIG. 13	
BY	CLASS	SUBCLASS
DRAFTSMAN	370	56

1/28
NSC1-62100

03/147,359
09/173,582

5566169



APPROVED
BY
DRAFTSMAN

O.G. FIG.
CLASS
SUBCLASS

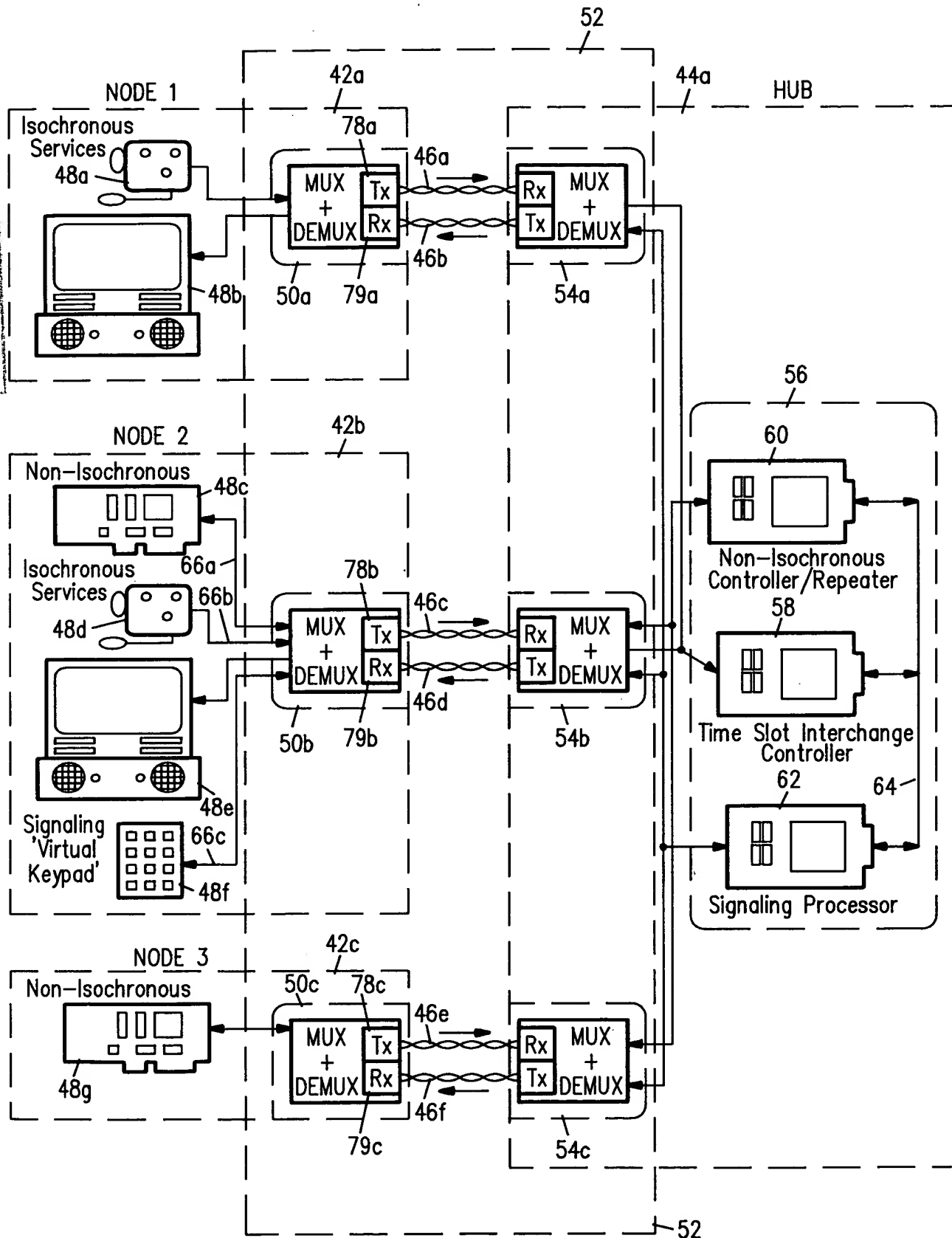


FIG. 2

APPROVED	O.G. FIG.
BY	CLASS
DRAFTSMAN	SUBCLASS

3/28
NSC1-62100

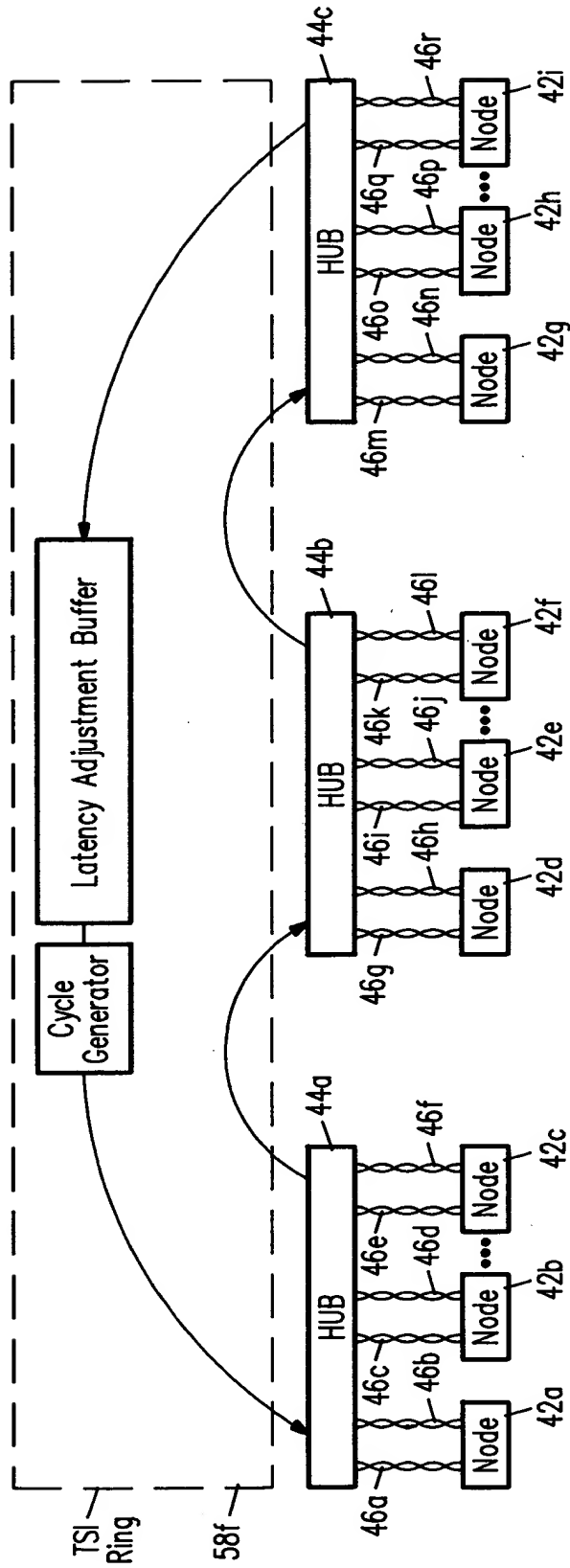


FIG. 3A

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

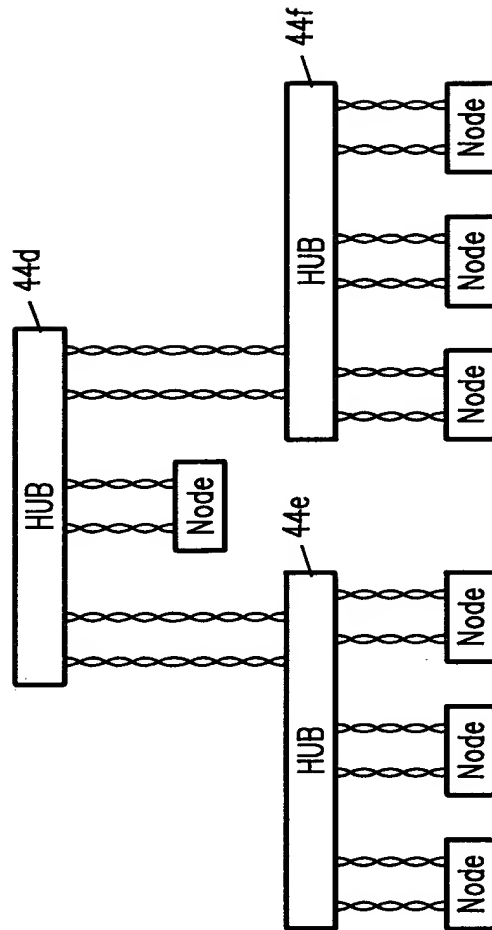


FIG. 3B

O.G. FIG.	
APPROVED BY	CLASS
DRAFTSMAN	SUBCLASS

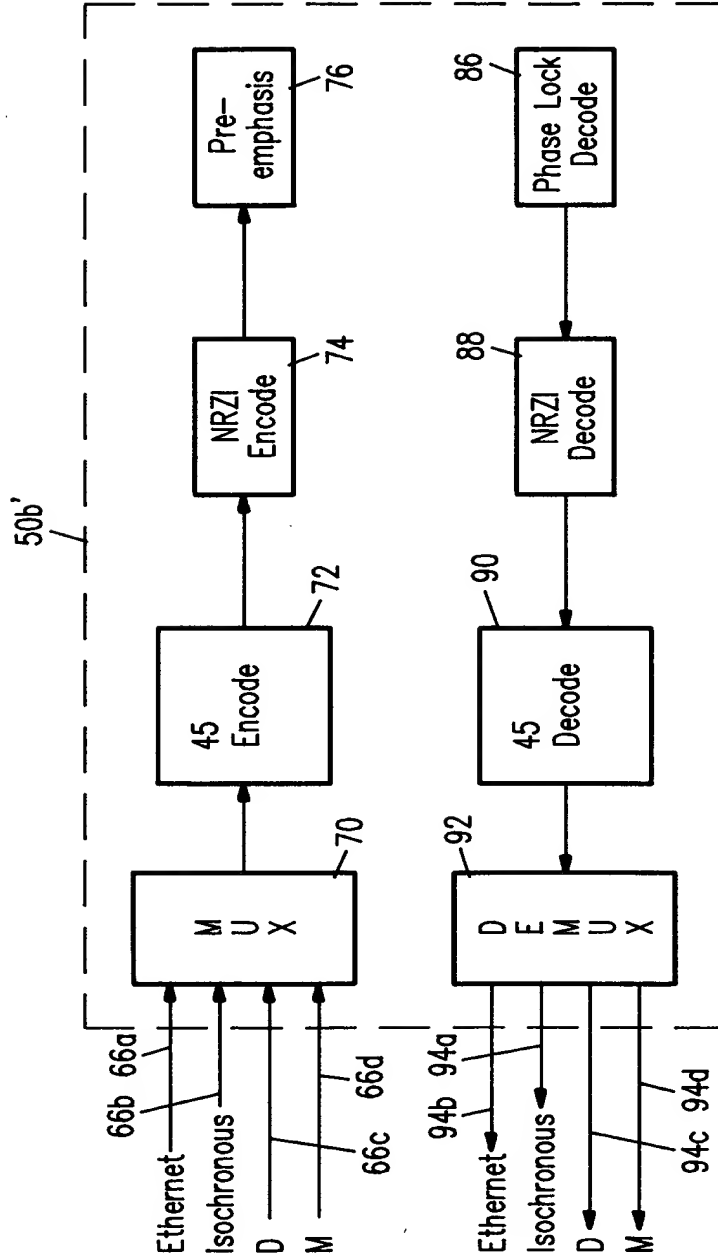


FIG. 4

O.G. FIG.		
APPROVED	BY	CLASS
CRAFTSMAN		

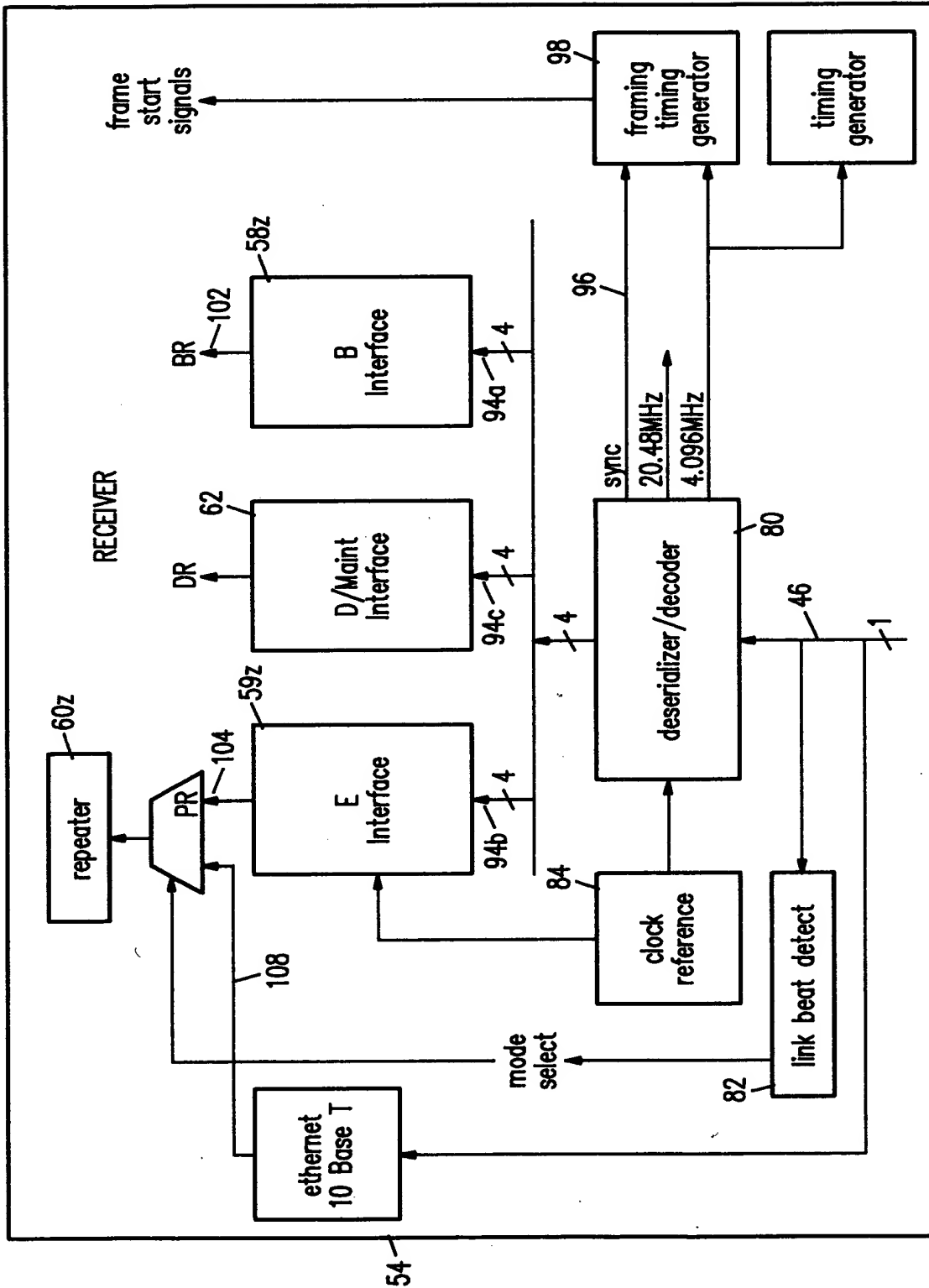


FIG. 5

O.G. FIG.		
APPROVED	BY	CLASS
		SUBCLASS
DRAFTSMAN		

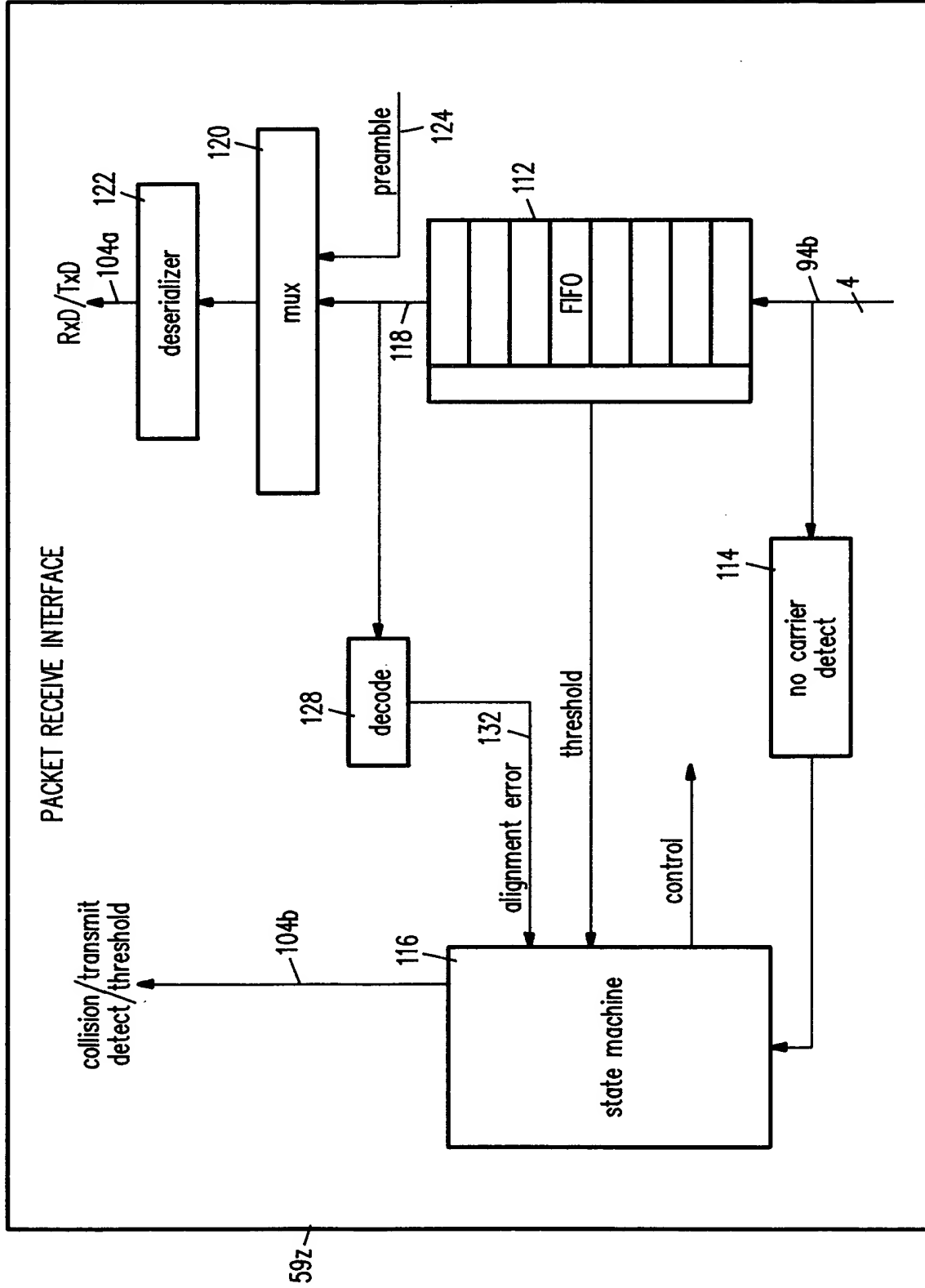


FIG. 6

APPROVED
BY
DRAFTSMAN

O.G. FIG.
CLASS
SUBCLASS

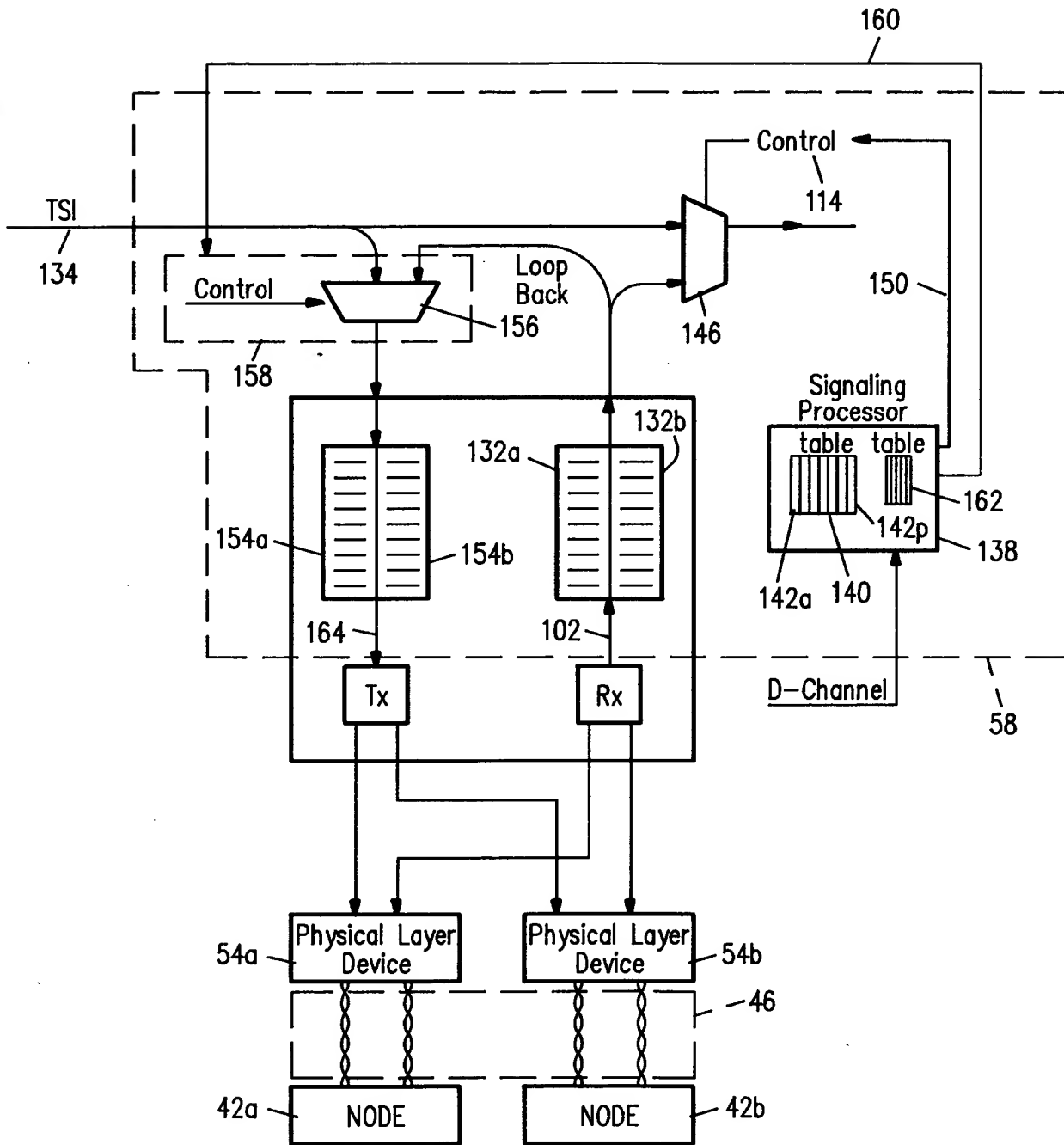


FIG. 7

APPROVED	0.6, FIG.
BY	CLASS
CRAFTSMAN	SUBCLASS

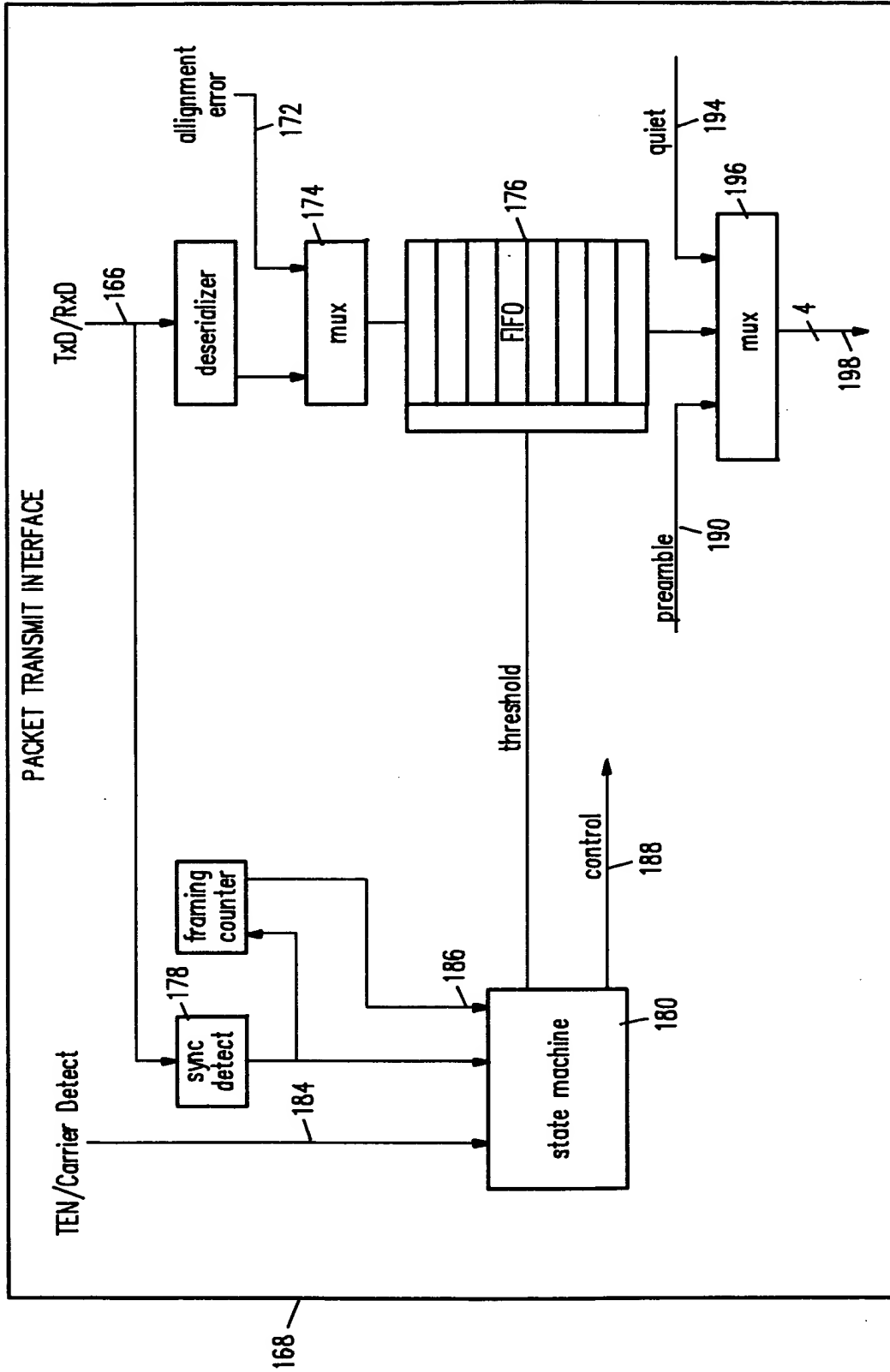


FIG. 8

APPROVED	O.G. FIG.
BY	CLASS
DRAFTSMAN	SUBCLASS

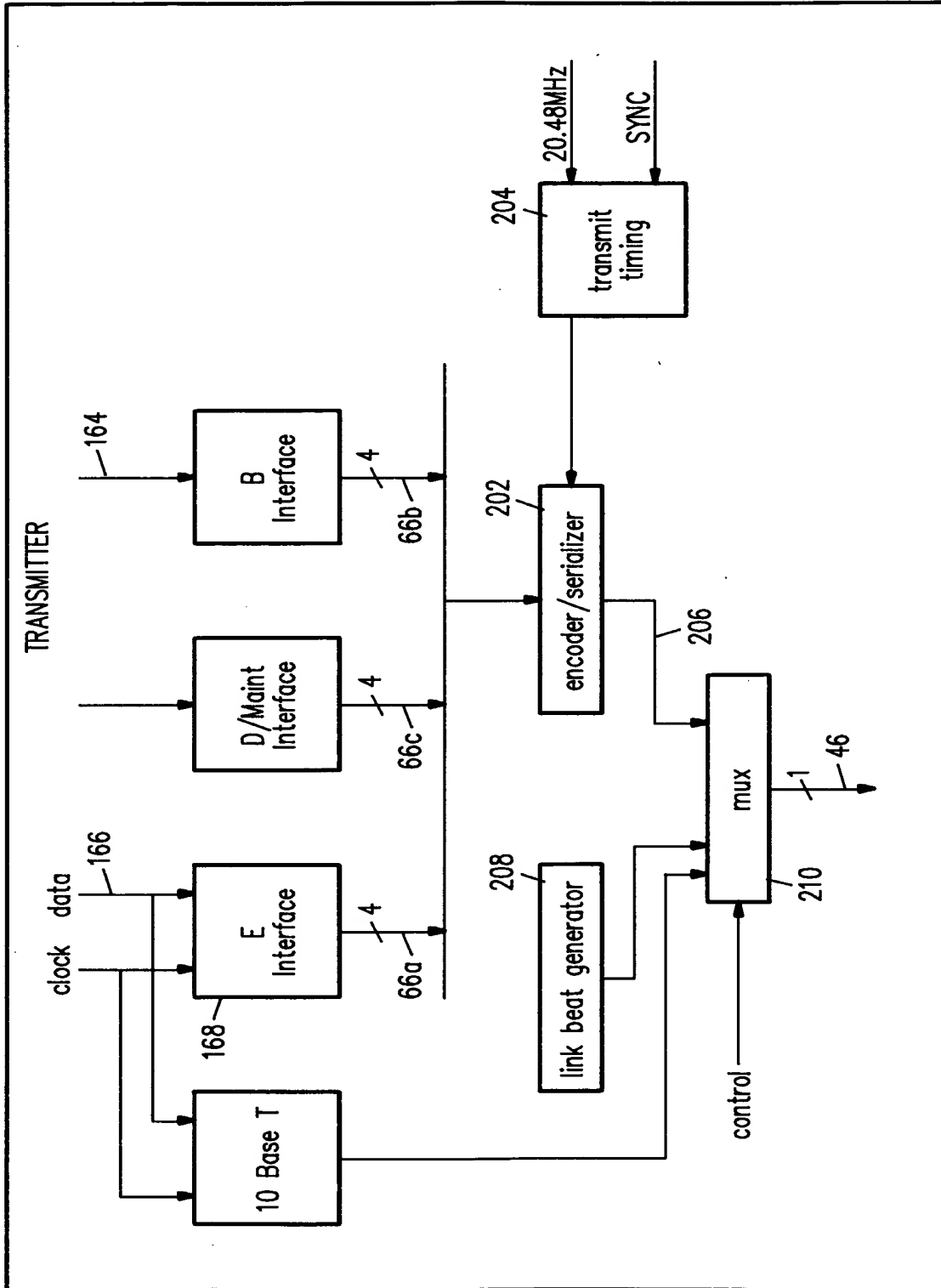


FIG. 9

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
CRAFTSMAN		

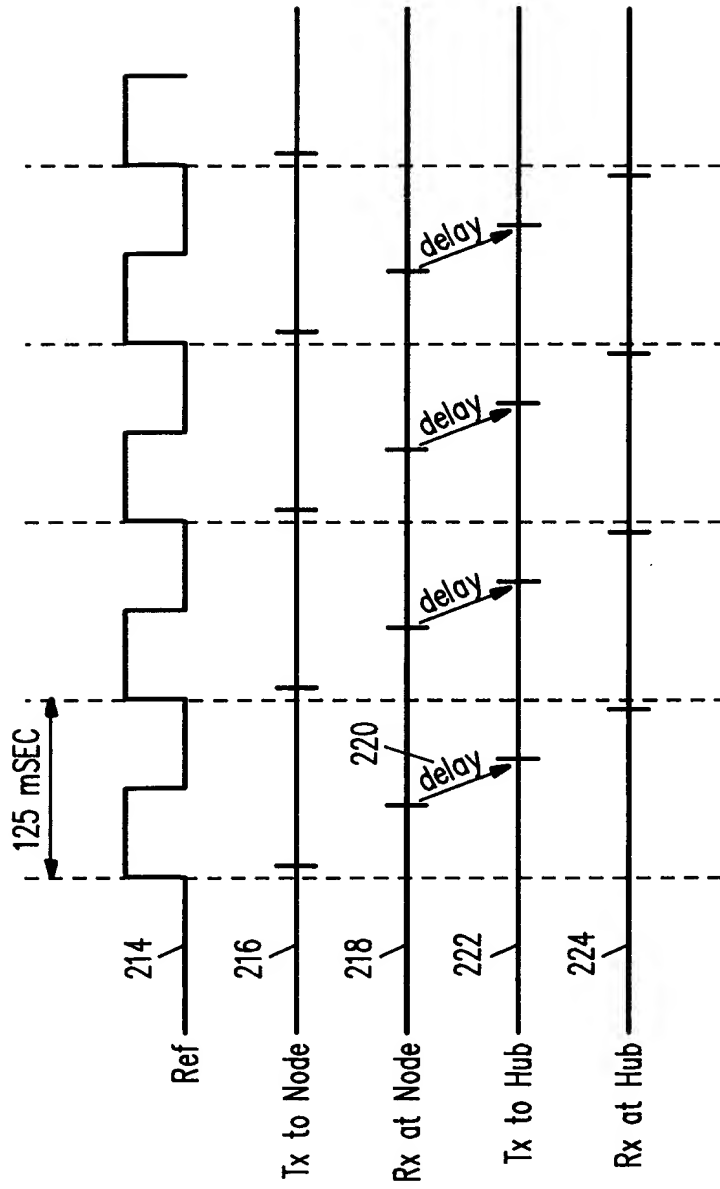


FIG. 10

APPROVED	O.G. FIG.
BY	CLASS
DRAFTSMAN	SUBCLASS

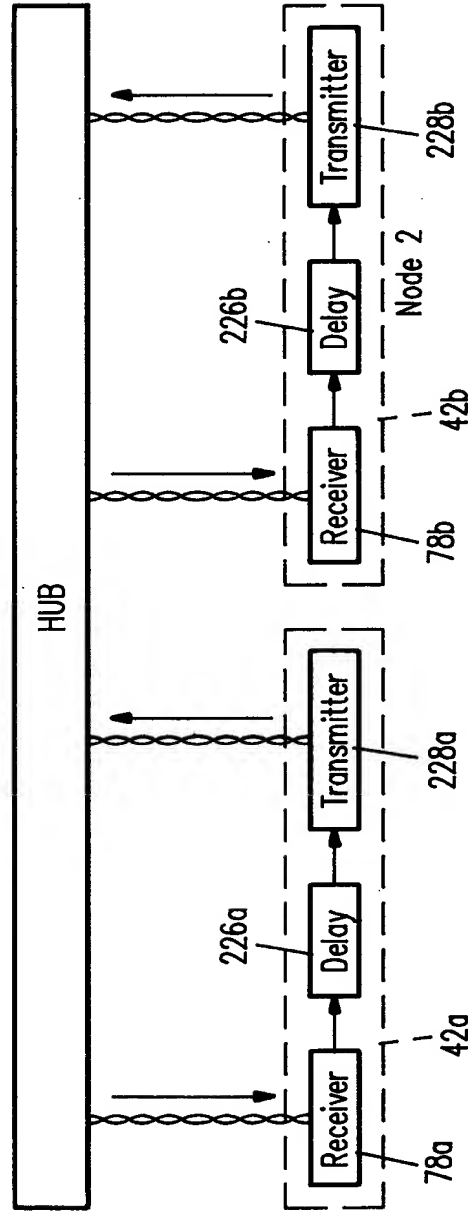


FIG. 11

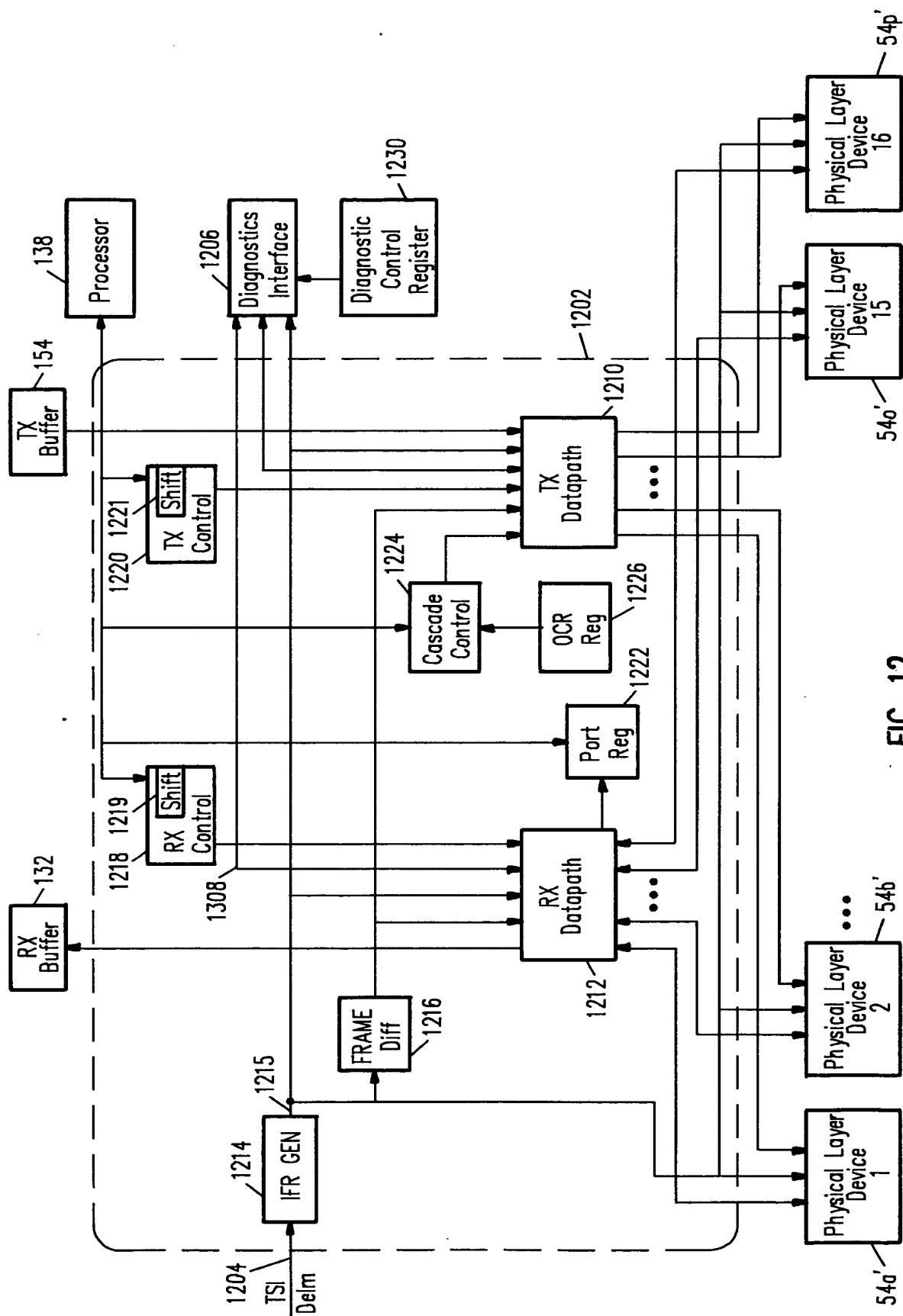


FIG. 12

APPROVED BY CRAFTSMAN
O.G. FIG. 13
CLASS: SUBCLASS
37056

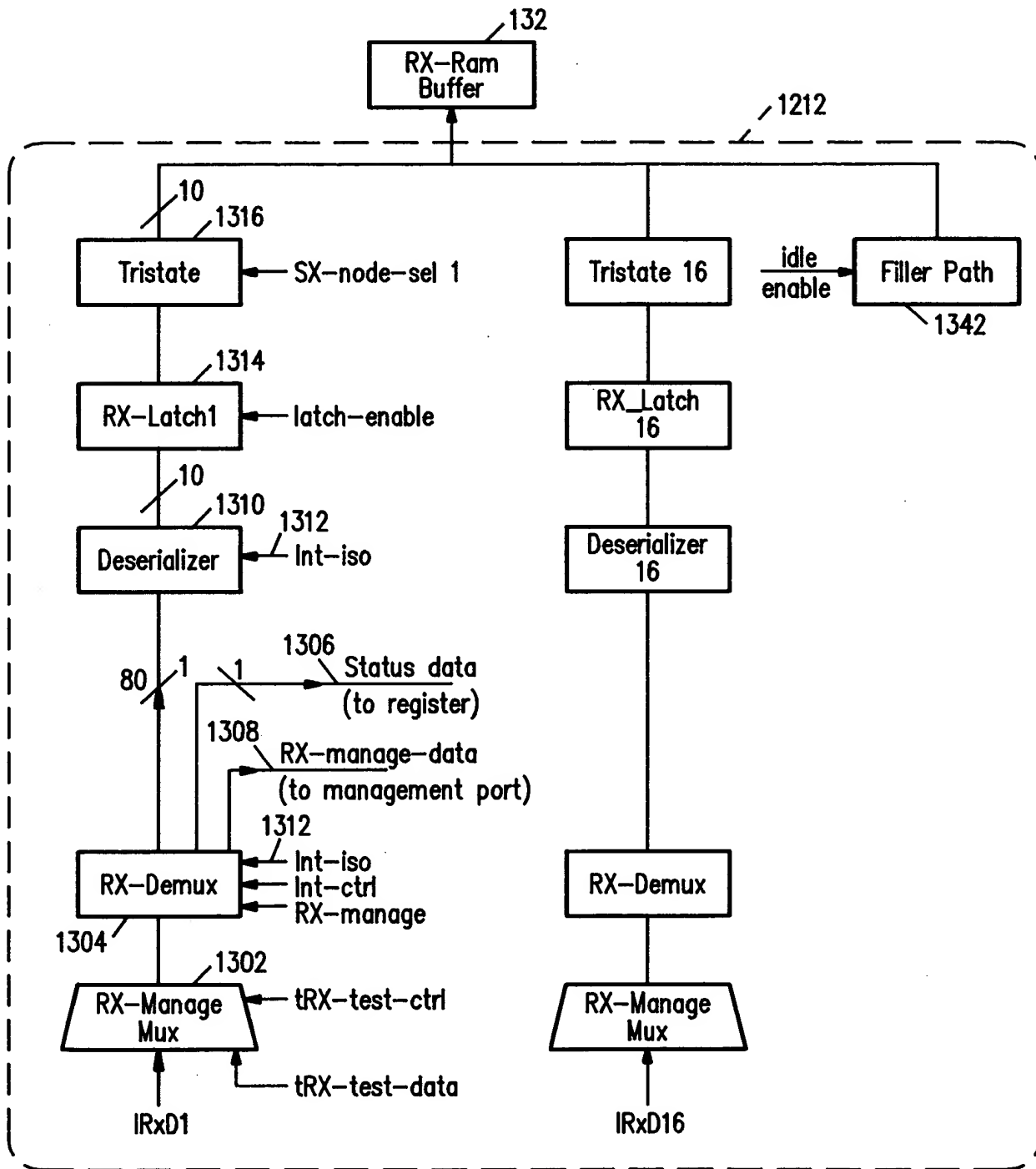


FIG. 13

APPROVED O.G. FIG.
BY GLASS SUBCLASS
CRAFTSMAN

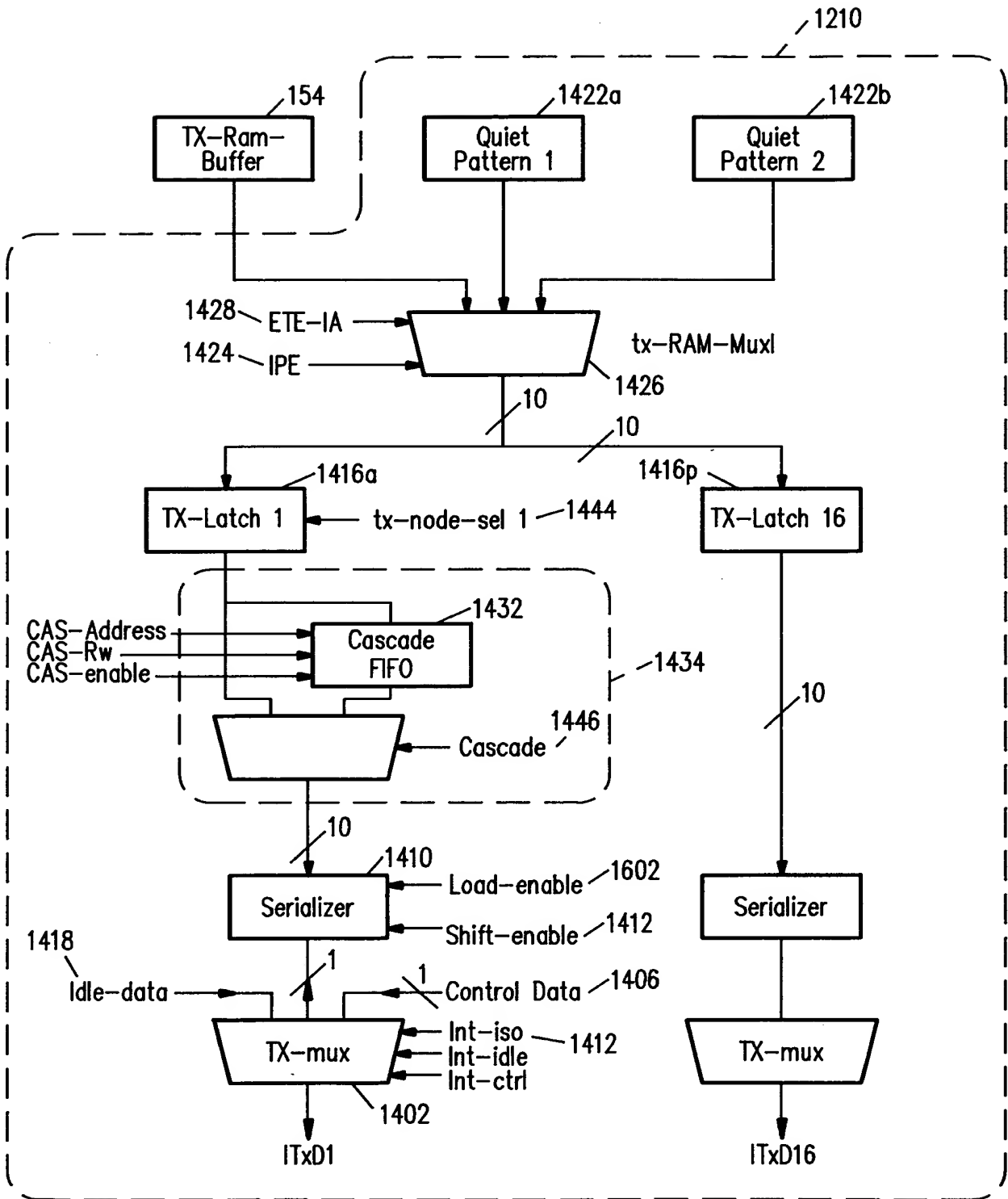


FIG. 14

1215

IFR

| 8 |

TXD | CTRL | B1 | IDLE | B2 | IDLE | B3 | | B96 | IDLE | IDLE | CTRL |

| 6 | 10 | 10 |

RXD

| STAT | B1 | IDLE | B2 | IDLE | B3 | | B96 | IDLE | IDLE | STAT |

2560 Clock Cycles
125 μ s

TXD: Data sent from Isochronous Data Exchanger to Physical Layer Portion.

RXD: Data Received by Isochronous Data Exchanger from Physical Layer Portion.

IFR: Isochronous Frame Sync signal sent from Isochronous Data Exchanger to Physical Layer Portion.

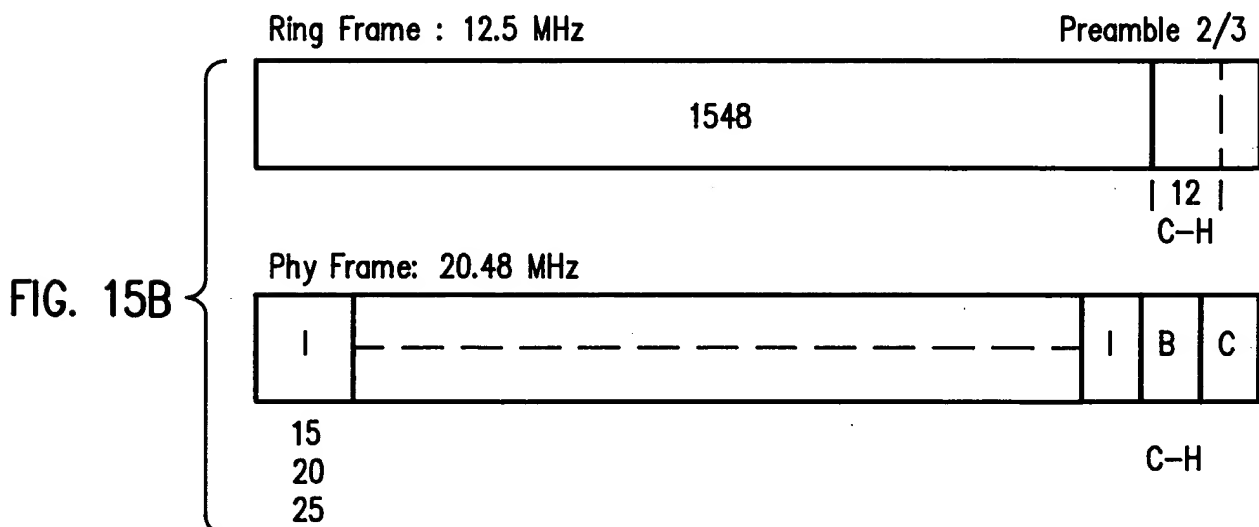
CTRL: Control data sent from Isochronous Data Exchanger to Physical Layer Portion.

STAT: Status data sent from Physical Layer Portion to Isochronous Data Exchanger.

B(1:96): B channel data (96 bytes of Bchannel data per μ s cycle).

IDLE: Filler data.

FIG. 15A



APPROVED	O.G. FIG.	
	CLASS	SUBCLASS
BY		
DRAFTSMAN		

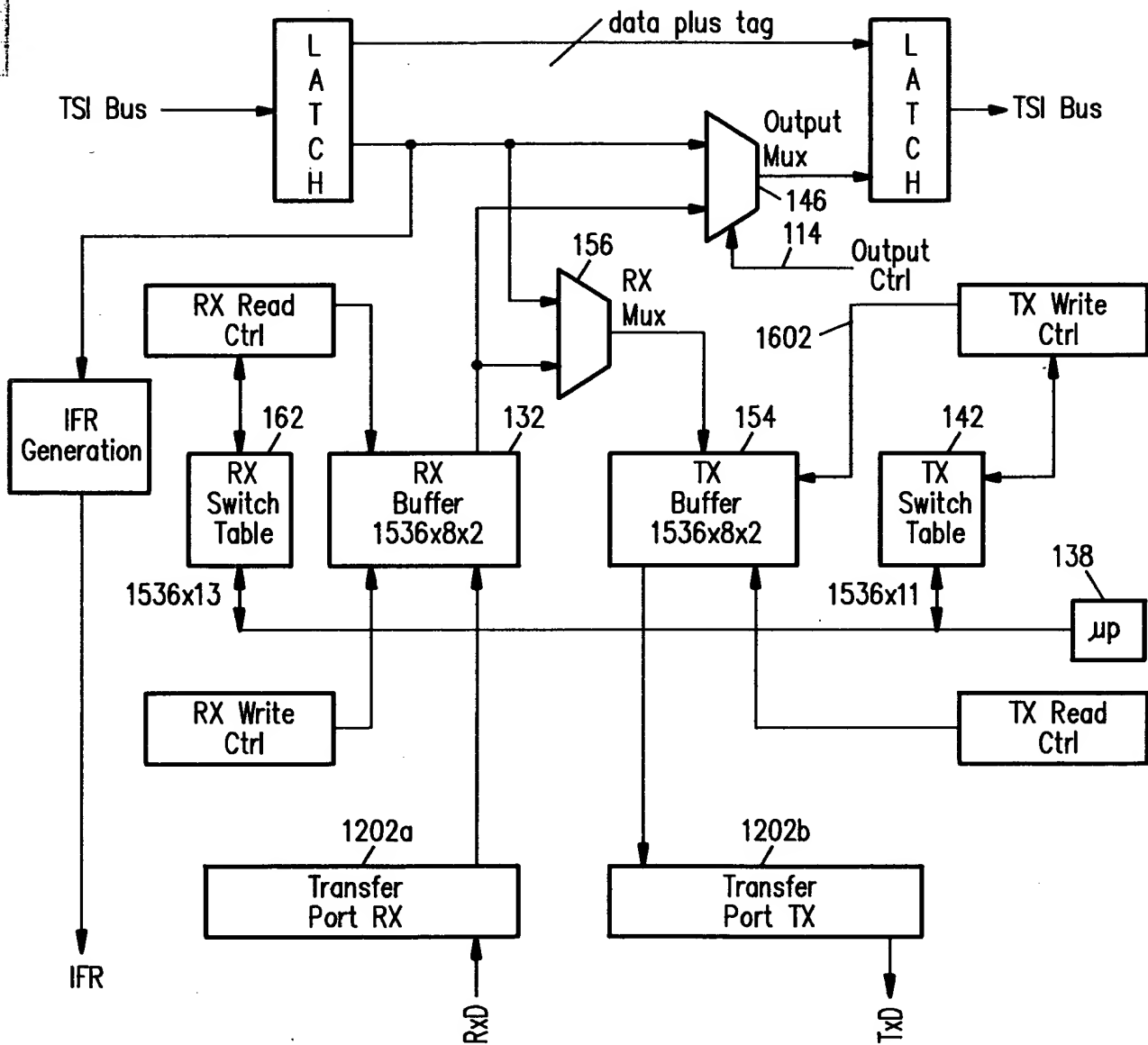


FIG. 16

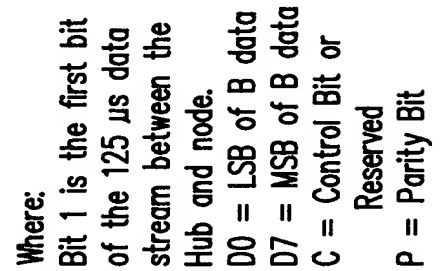
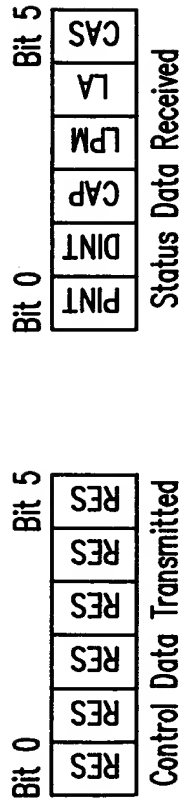


FIG. 17

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		



Control Bits

RES: Reserved bit.

Status Bits

CAS: Cascade bit: Used to activate the port 1 cascade logic.

LA: Link Active: Indicates that the link is isochronous active when set.

LPM: Low Power Mode: Indicates that the isophy is in low power mode when set.

CAP: CAPacity: Indicates the type of Isochronous capacity.
 "1" 15.872 Mbps Isochronous bandwidth
 "0" 6.144 Mbps Isochronous bandwidth

DIN T: D INInterrupt: Indicates that the isophy has received a start of D channel packet when set.

PIN T: M INInterrupt: Indicates that the isophy's maintenance has changed when set.

FIG. 18

APPROVED.	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Symbol Deletion

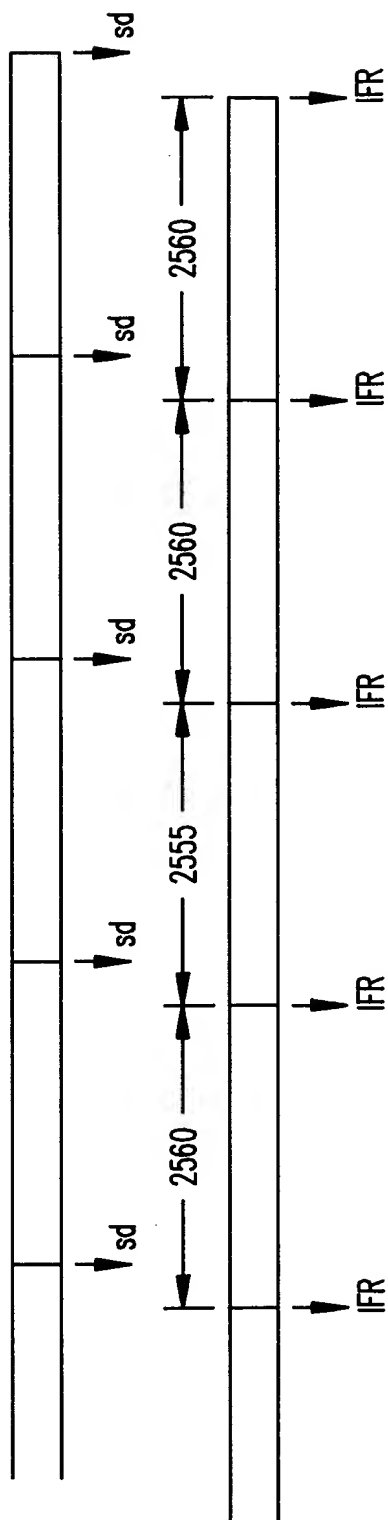


FIG. 19A

Symbol Addition

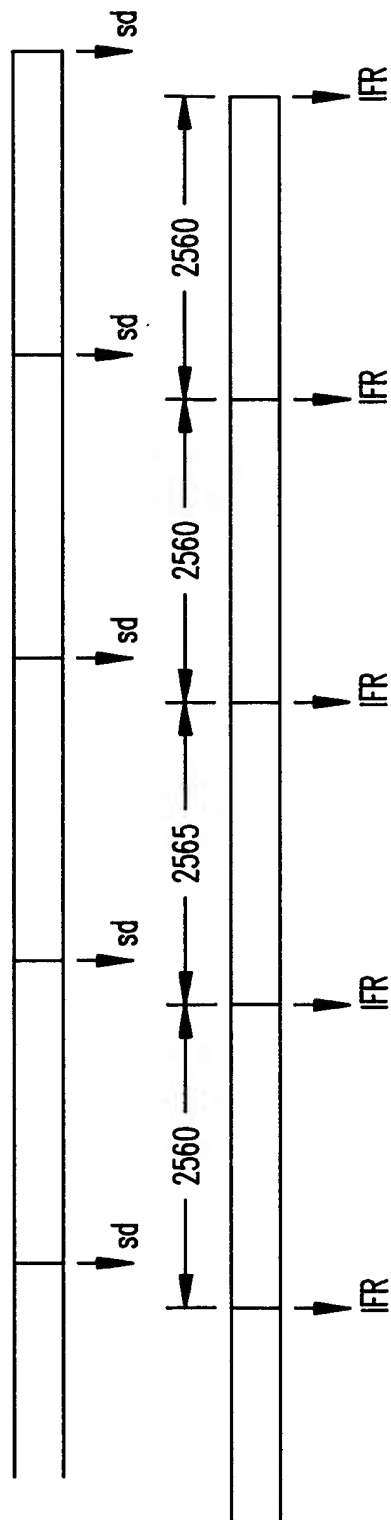


FIG. 19B

APPROVED	O.G. FIG.
BY	CLASS
DRAFTSMAN	SUBCLASS

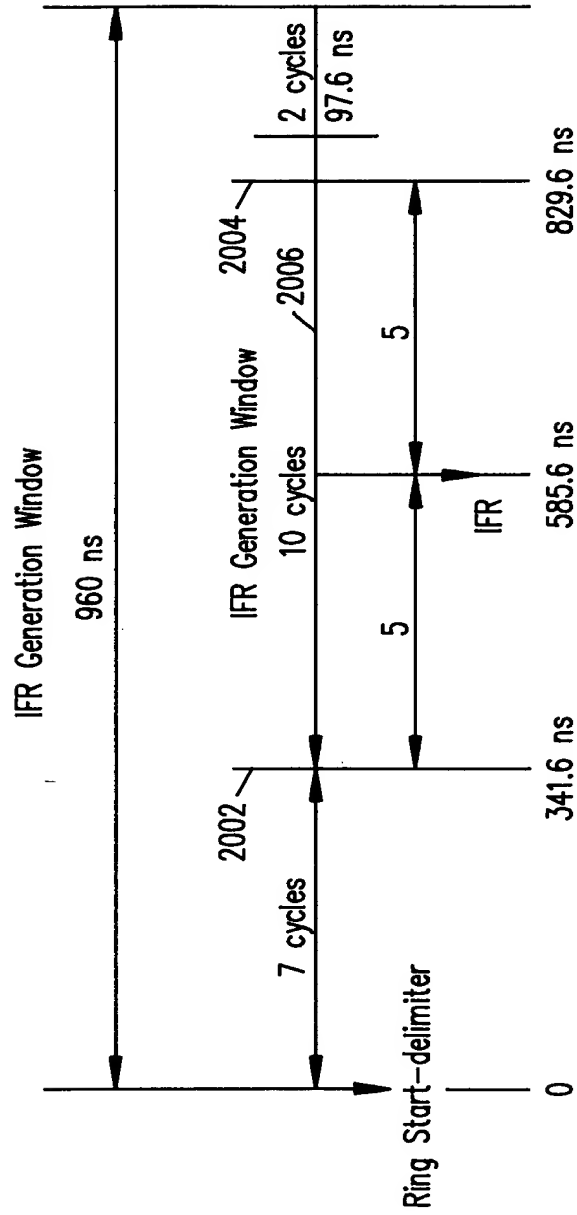


FIG. 20

O.G. FIG.	
APPROVED	BY CLASS SUBCLASS
CRAFTSMAN	

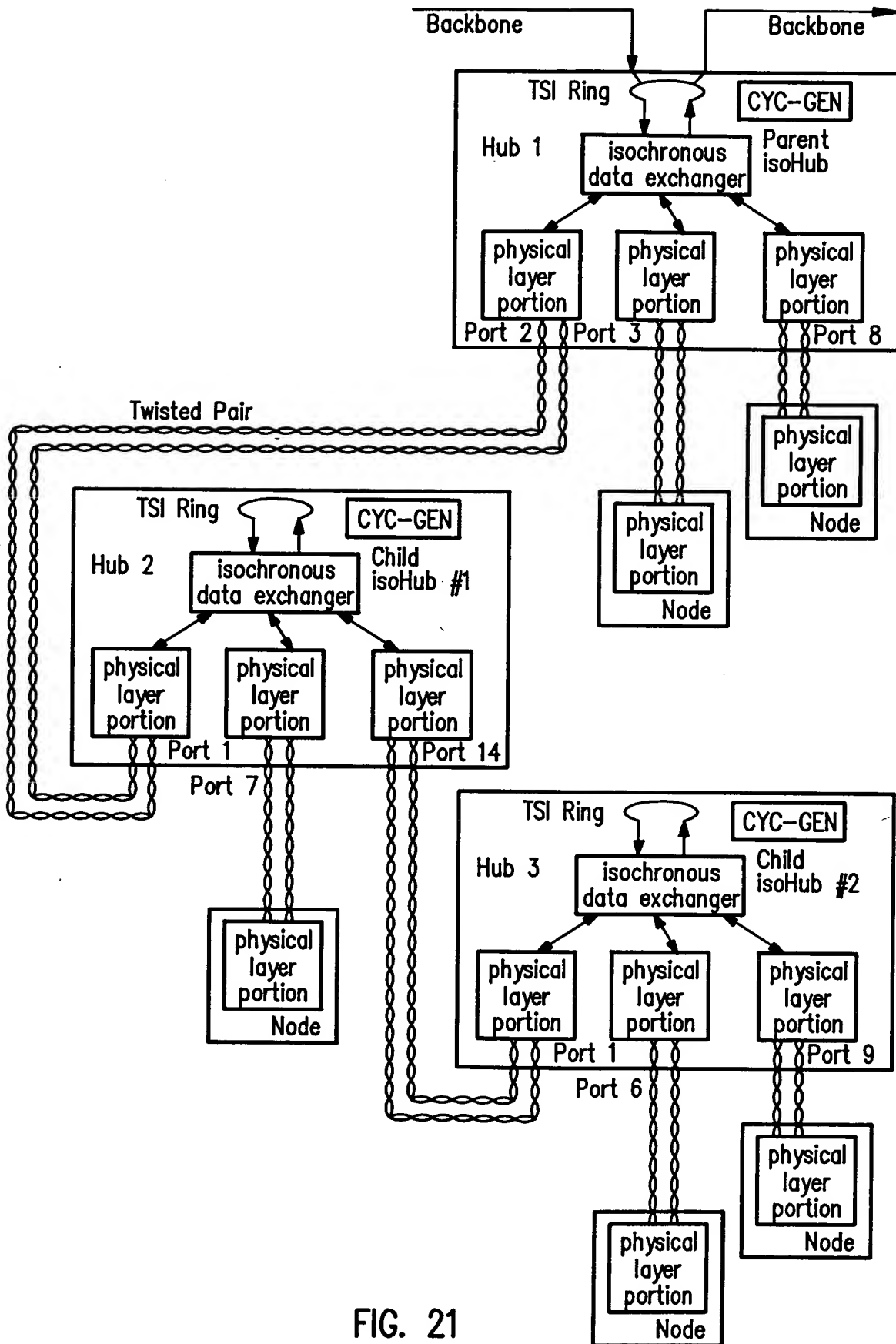


FIG. 21

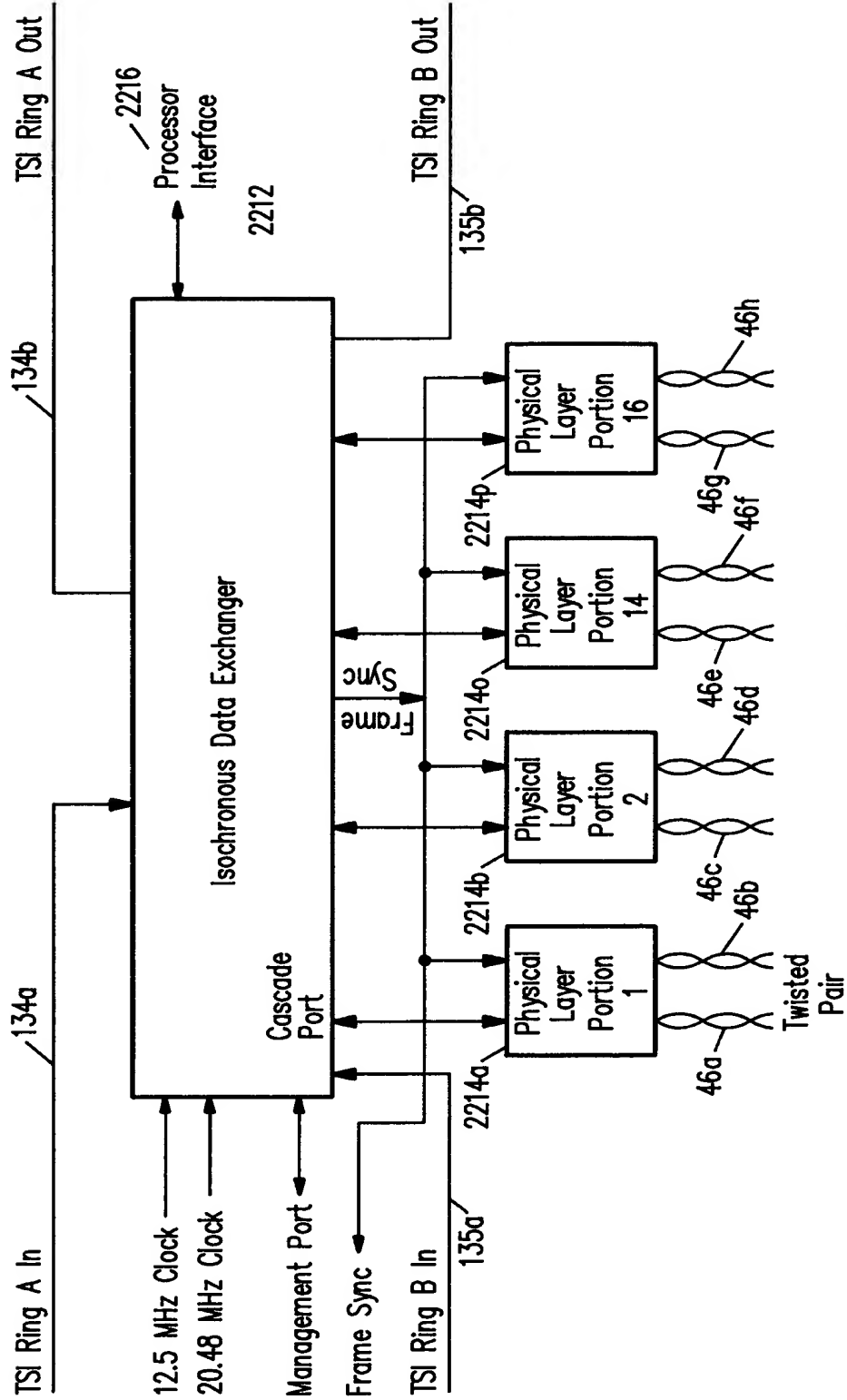


FIG. 22

APPROVED BY CRAFTSMAN
O.G. FIG. CLASS SUBCLASS

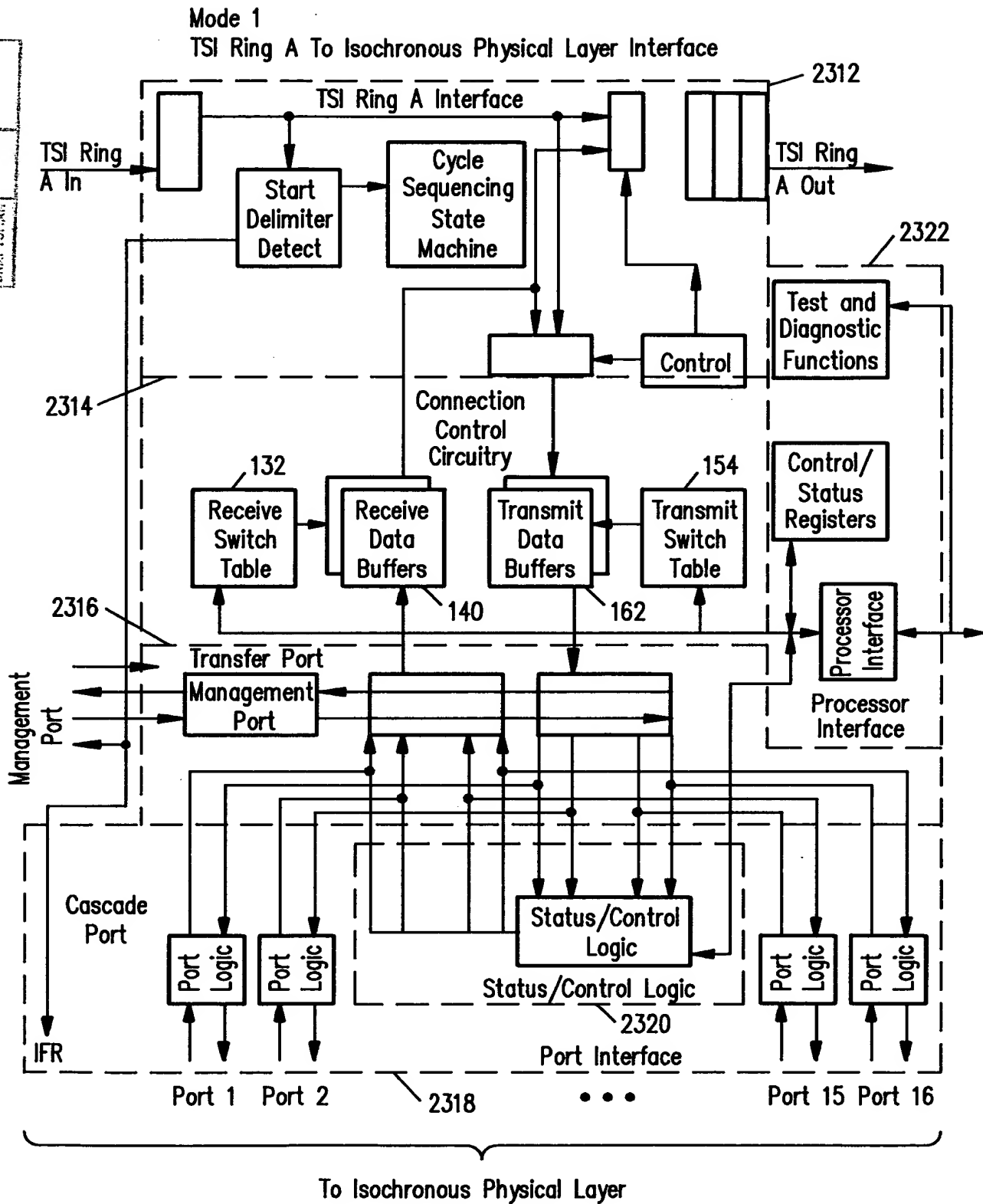


FIG. 23A

APPROVED	O.G. FIG.	
	BY	CLASS
DRAFTSMAN	SUBCLASS	

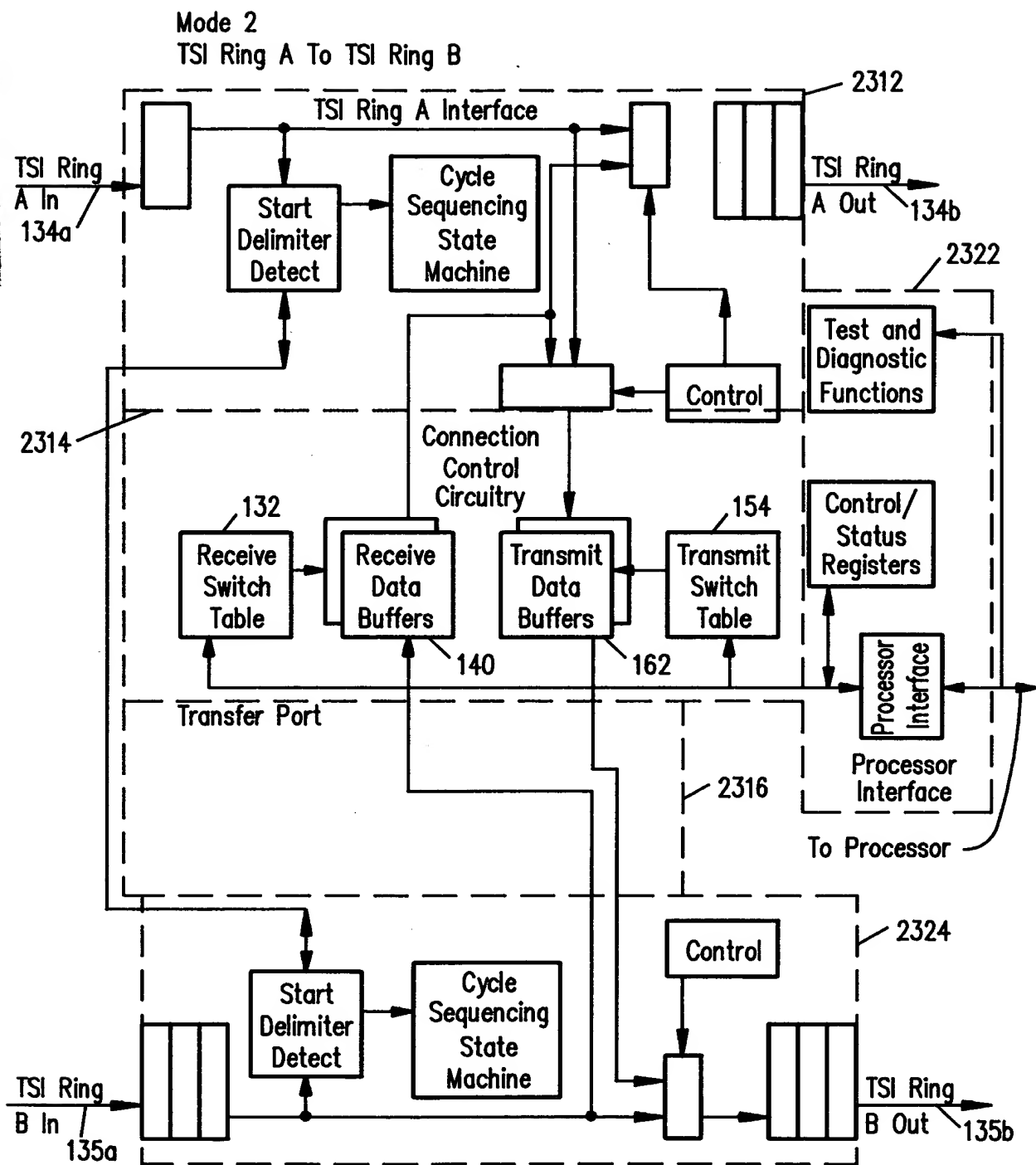
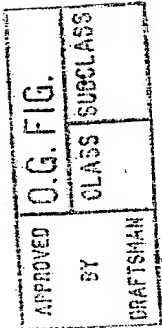


FIG. 23B



Switch Table Address

Isochronous Maintenance Channel (IMC)

TSI Ring A Slot 1

TSI Ring A Slot 2

⋮

TSI Ring A Slot 1535

TSI Ring A Slot 1536

0

1

2

⋮

1535

1536

Receive Switch Table

Parity	TSE	ITE	ETE	Data Buffer Address
				⋮

MSB

1 Bit

1 Bit

1 Bit

1 Bit

← 11 Bits →

LSB

FIG. 24A

Switch Table Address

Not Used

Port 1, B channel 1

Port 2, B channel 1

⋮

Port 14, B channel 96

Port 2, B channel 96

0

1

2

⋮

1535

1536

Transmit Switch Table

Parity	Not Used	IPE	IA	Data Buffer Address
				⋮

MSB

1 Bit

1 Bit

1 Bit

1 Bit

← 11 Bits →

LSB

FIG. 24B

Bit Definitions

IA: Idle Address:

ITE: Internal Transmit Enable:

IPE: Idle Pattern Enable:

Indicates the idle pattern to be sent.

Indicates on Internal loopback of the slot when set.

Indicates the use of a quiet pattern when set.

Switch Table Address

Isochronous Maintenance Channel (IMC)

TSI Ring A Slot 1

TSI Ring A Slot 2

⋮

TSI Ring A Slot 1535

TSI Ring A Slot 1536

0

1

2

⋮

1535

1536

Receive Switch Table

Parity	TSE	ITE	ETE	Data Buffer Address
				⋮

MSB

1 Bit

1 Bit

1 Bit

1 Bit

← 11 Bits →

LSB

FIG. 25A

Switch Table Address

Isochronous Maintenance Channel (IMC)

TSI Ring B Slot 1

TSI Ring B Slot 2

⋮

TSI Ring B Slot 1535

TSI Ring B Slot 1536

0

1

2

⋮

1535

1536

Transmit Switch Table

Parity	TSE	Not Used	ETE	Data Buffer Address
				⋮

MSB

1 Bit

1 Bit

1 Bit

1 Bit

← 11 Bits →

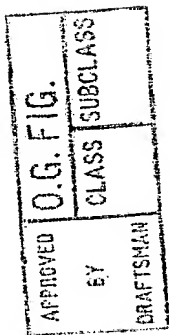
LSB

FIG. 25B

Bit Definitions

ETE: External Transmit Enable: In Mode 2, indicates an External switching of slot when set.

TSE: Tri-State Enable: The isoTSX drives the TSI ring output drivers when set.



APPROVED	O.G. FIG.
BY	CLASS
DRAFTSMAN	SUBCLASS

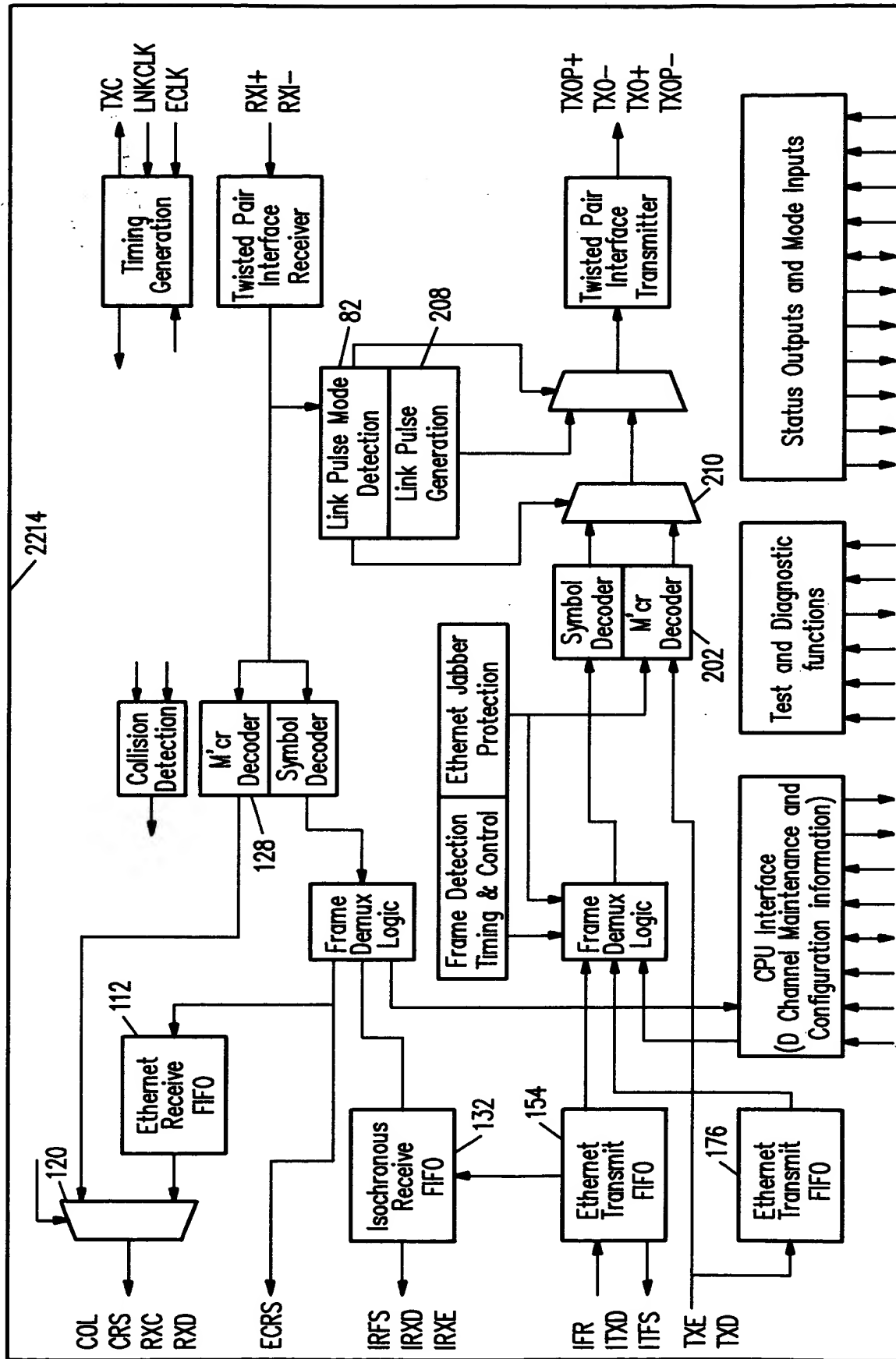


FIG. 26